V. P. & R. P. T. P. Science College
First Internal Exam
US04CELE-02

13/03/14
1.00 to 2.30 pm
Total Marks 30

Q. 1 Answer any **three** questions in short.   06

1. Define Fan –in and Fan out of Circuit.
2. Name the logic families you know?
3. What are advantages of D Flip Flop over RS Flip Flop?
4. What is RC differentiation?
5. What is the advantage of serial counter?
6. What is disadvantage of parallel counter?

Q.2   Describe any three applications of X-OR gate.   08

OR

Q.3 Describe in detail working of TTL NAND gate with logic specifications.   08

Q.3 Describe in detail Edge triggered D Flip Flop.   08

OR

Q.4   Describe fully Schmitt Trigger circuit.   08

Q.4   Explain working of 8bit Asynchronous counter. Draw waveform diagram also.   08

OR

Q.5   Explain working of Mod 6 parallel counter. Draw waveform diagram also.   08

***************************BEST OF LUCK *************************