V. P. & R. P. T. P. Science College First Internal Exam US04CELE-02

16/03/18 3:00 p.m to 4.30 p.m. Total Marks 25

Q.1 Multiple Choice Questions: 1. Comparator output is high i) If two input data is same ii) If two input data is complementary iii) If two input data is different 2. Flip Flop is i) Astable multivibrator ii) Bistable multivibrator iii) Monostable multivibrator 3. A counter is a circuit that counts Number of clock pulses that hit the Counter i) ii) Time period of Clock Pulse iii) None of above Answer any two questions in short. Q. 2 04 1. Name various logic specifications. 2. What is RC differentiation? 3. What is the advantage of combinational counter? Q.3 Describe any two applications of X-OR gate. 06 OR Q.3 Describe in detail working of TTL NAND gate with neat diagram. 06 Q.4 Describe in detail JK Flip Flop. 06 OR Describe fully Schmitt Trigger circuit. Q.4 06 Explain working of 8bit serial counter. Q.5 06 OR Explain working of Mod 7 parallel counter. Q.5 06

*****BEST OF LUCK *****