

**V.P. & R.P.T.P. SCIENCE COLLEGE**  
**T.Y.B.Sc. (SEMESTER – VI)**  
**FIRST INTERNAL TEST : MAR-2014**

**US06CCSC04 : Introduction to Microprocessors and Assembly Language**  
**DATE : 13/03/2014                      TIME : 03:30PM to 05:00PM                      MARKS : 30**

<b>Q.1</b>	<b>Answer the following in short. (Any Three)</b> <b>1. What do you know about Carry Flag (CF)?</b> <b>2. What is EQU?</b> <b>3. Explain the MOV instruction.</b> <b>4. Explain the XCHG instruction.</b> <b>5. What is the use of LOOP instruction?</b> <b>6. What is CLD processor control instruction?</b>	<b>06</b>
<b>Q.2</b>	<b>(a) Explain in brief the Segment Registers.</b> <b>(b) Explain the ASSUME and DT Assembler Directives.</b> <p style="text-align: center;"><b>OR</b></p>	<b>04</b> <b>04</b>
<b>Q.2</b>	<b>Write detailed note on 8086 internal architecture : Execution unit and Bus Interface unit</b>	<b>08</b>
<b>Q,3</b>	<b>Explain the following Arithmetic Instructions with examples.</b> <b>(a) ADC    (b) IMUL    (c) DIV</b> <p style="text-align: center;"><b>OR</b></p>	<b>08</b>
<b>Q.3</b>	<b>Explain the following Logical Instructions with examples.</b>	<b>08</b>
<b>Q.4</b>	<b>Explain the following string instructions</b> <b>(a) SCASB                      (b) LODSW                      (c) STOS</b> <p style="text-align: center;"><b>OR</b></p>	<b>08</b>
<b>Q.4</b>	<b>Explain the following jump instructions</b> <b>(a) JNL                      (b) JCXZ                      (c) JO</b>	<b>08</b>

