

V.P. & R.P.T.P SCIENCE COLLEGE
INTERANAL EXAM
T.Y.B.Sc (SEM-VIth) ELECTRONICS
SUB. CODE:-US06CELE03

DATE:-14/03/2018

Time:- 11:00 am to 12:30 pm

MARKS-25

Q-1 Choose correct answer

[3]

- Maximum time delay using single register program is _____.
(A) 1 sec. (C) 1.8 ms
(B) 1.8 sec. (D) 1.9 ms
- To design counter and time delay _____ and _____ techniques are used.
(A) Looping, Counting (C) Nesting, Subroutine
(B) Debugging, Indexing (D) none of above
- If accumulator (A) = 39 H, after execution of ANI F0 H, the contain of Accumulator is _____.
(A) 30 H (C) 03 H
(B) 39 H (D) 93 H



Q-2 Short answer type question.(attempt any two)

[4]

- Define counter and time delay.
- List arithmetic instructions related to memory in 8085 system.
- Which instructions are used to stored and retrieves data from STACK?

Q-3 Fifteen byte of data is stored in memory location starting at XX70. Write a programme to add all the data bytes and save the carry generated in a register. Display the entire sum at any two output ports.

[6]

OR

Q-3 What do you mean by debugging a program? Discuss different technique of dynamic debugging.

[6]

Q-4 Discuss different STACK and Subroutine instructions with illustration.

[6]

OR

Q-4 Write a programme to count continuously from 0 to 9 with 0.1 second delay between each count. Use register pair to setup the delay and display each count at one of the output port. Assume the clock period is 0.5 μ s and number of loop T-state is 15.

[6]

Q-5 Write a programme to convert two digit BCD number stored in memory location to its equivalent binary number.

[6]

OR

Q-5 Write a programme to convert a binary number stored in memory location to its equivalent un-packed BCD number.

[6]