

# V.P. & R.P.T.P SCIENCE COLLEGE

## INTERANAL EXAM

### T.Y.B.Sc(SEM-V) ELECTRONICS

SUB: - 8085 Micro processor system

SUB CODE: US05CELE03

DATE:-05/10/2019

TIME: 11:00 pm to 12:15 pm

MARKS-25

Q-1 Choose correct answer [05]

1. 8085 microprocessor has \_\_\_\_\_.  
(A) 40 pin (C) 8 pin  
(B) 20 pin (D) none of above
2. \_\_\_\_\_ is the 16-bit register in 8085  $\mu$ p.  
(A) stack pointer (C) accumulator  
(B) flag register (D) none of above
3. Following are control signals in 8085  $\mu$ p.  
(A)  $\overline{WR}$  and  $\overline{RD}$  (C)  $\overline{SOD}$  and  $\overline{SID}$   
(B)  $AD_0$  (D) none of above
4. JNC is \_\_\_\_ byte instruction.  
(A) two (C) three  
(B) one (D) none of above
5. Which of following is two bytes instruction?  
(A) MVI B,09 H (C) JMP 2009 H  
(B) MOV C,A (D) none of above



Q-2 Draw the architectural block diagram of 8085  $\mu$ p and list name of each section. [05]

OR

Q-2 Discuss the concept of Bus Timing with necessary diagram [05]

Q-3 Explain classification of instruction of 8085  $\mu$ p with suitable illustration. [05]

OR

Q-3 Explain method of writing, assembling and executing a simple program in 8085  $\mu$ p. [05]

Q-4 Discuss different logical instructions of 8085  $\mu$ p with illustration. [05]

OR

Q-4 Write a programme to load two numbers in two registers now subtract one number from other such that carry flag will set and display the answer at output port. [05]

Q-5 Describe conditional and un-conditional jump instructions giving suitable examples. [05]

OR

Q-5 Discuss different additional data transfer instructions of 8085  $\mu$ p system with illustration of each. [05]