

V.P. & R.P.T.P SCIENCE COLLEGE

INTERANAL EXAM

T.Y.B.Sc.(SEM-V) INSTRUMENTATION(V)

SUB: - 8085 Microprocessor system – I

SUB CODE: US05CINV05

DATE:-09/10/2019

TIME: 11:00 pm to 12:15 pm

MARKS-25

Q-1 Choose correct answer [05]

1. The content of accumulator is A5 H, after execution of CMA instruction it becomes

- (A) 55 H (C) A5 H
(B) AA H (D) none of above

2. _____ Flag is affected during data transfer operation.

- (A) Carry (C) Zero
(B) Sign (D) none of above

3. The address buses of 8085 μ p contain _____ bit.

- (A) 4 (C) 16
(B) 8 (D) none of above

4. Which of following is two bytes instruction?

- (A) MVI B,0A H (C) JMP 2001 H
(B) MOV B,A (D) none of above

5. CALL and RET are _____ type instruction.

- (A) Advance (C) Logical
(B) Branch (D) none of above

Q-2 Discuss the concept of generating the control signals in 8085 μ p system. [05]

OR

Q-2 Draw the architectural block diagram of 8085 μ p and list its different section. [05]

Q-3 Explain classification of instruction Of 8085 μ p according to word size with suitable illustration. [05]

OR

Q-3 Explain the method of writing, assembling and executing a simple program in 8085 μ p. [05]

Q-4 Discuss different arithmetic instructions with suitable illustration. [05]

OR

Q-4 Write a programme: to load 7C H and 3B H in register C and D respectively. Now increment content of C than add both the number and display the sum at output port. [05]

Q-5 Discuss different 16 bit arithmetic instructions of 8085 μ p system with illustration of each. [05]

OR

Q-5 Write a program to load 9B H and A7 H in register B and C respectively. Now add both the numbers, if the sum is greater than FF H display 01 at output port 0, otherwise display the sum. [05]

